

In Place of FORM PTO-1449 (Modified)

Serial No. 10/671132

**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT**

Applicant: Accapadi, et. al.

Filing Date: 9/25/2003

Group: 2195

Atty. Docket No. AUS920030413US1

U. S. PATENT DOCUMENTS

Examiner <u>Initial</u>	Document <u>Number</u>	<u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Subclass</u>	Filing Date if <u>Appropriate</u>
_____	AA					
_____	AB					
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_____	AE					
_____	AF					
_____	AG					
_____	AH					
_____	AI					
_____	AJ					
_____	AK					
_____	AL					

FOREIGN PATENT DOCUMENTS

Examiner <u>Initial</u>	Document <u>Number</u>	<u>Date</u>	<u>Country</u>	<u>Class</u>	<u>Subclass</u>	Translation <u>Yes</u> <u>No</u>
_____	AM					
_____	AN					
_____	AO					
_____	AP					

Examiner
Initial

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<u>/A.K./</u>	AQ	Luo et al., "Balancing Throughput and Fairness in SMT Processors," IEEE, 2001, p. 164-171.
_____	AR	
_____	AS	
_____	AT	

Examiner: /Abdullah Al Kawsar/

Date Considered: 06/21/2007

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.